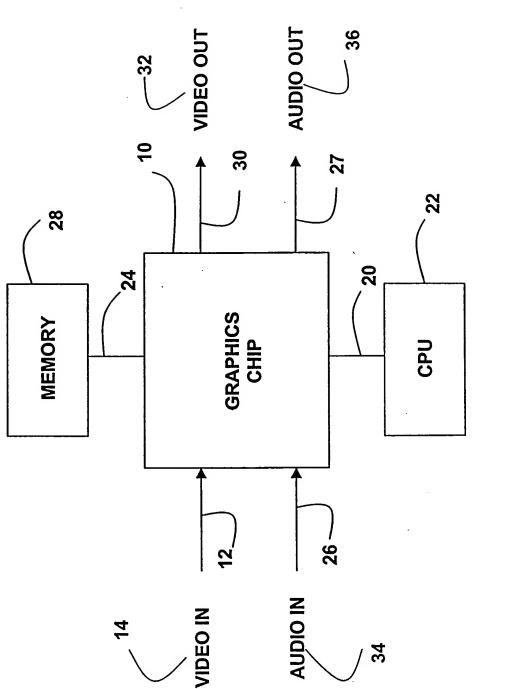
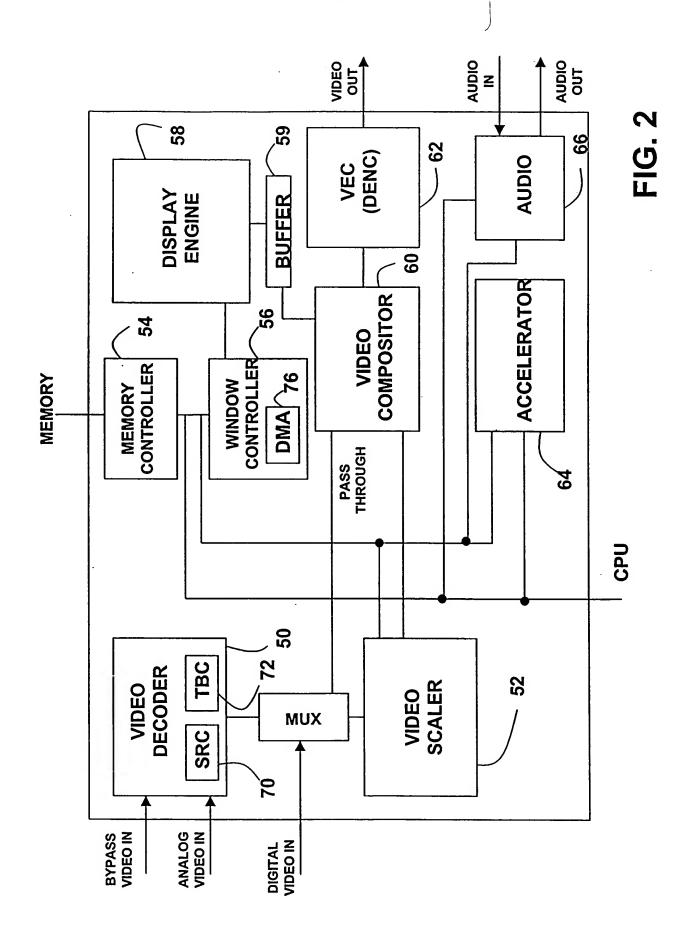
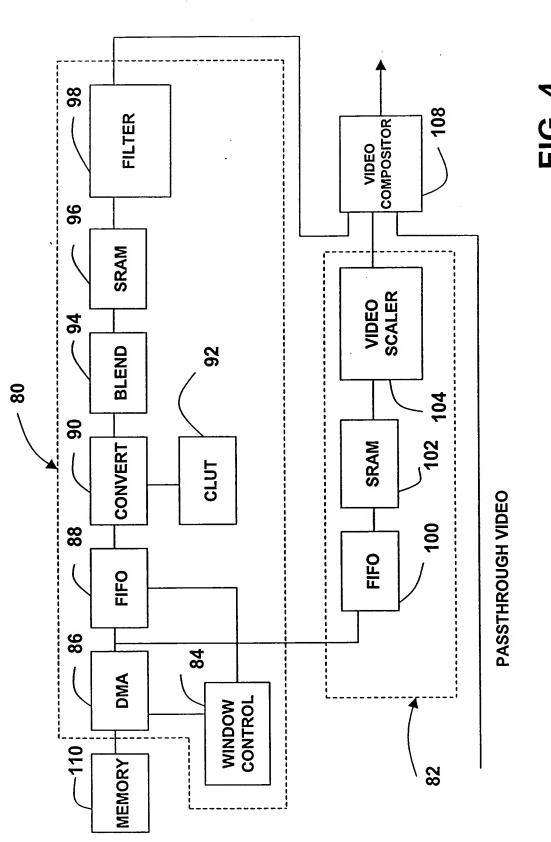
Attorney: Jun-Young E. Jeon
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Inventor(s): Alexander G. MacInnis, et al.
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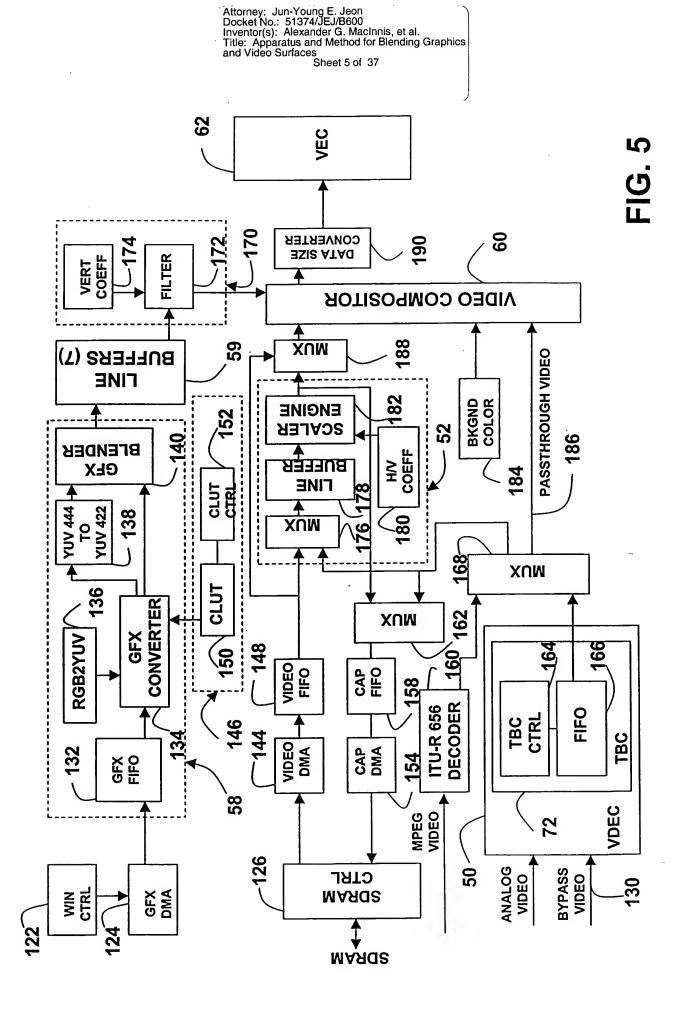
Attorney: Jun-Young E. Jeon
Docket No.: 51374/JEJ/B600
Inventor(s): Alexander G. MacInnis, et al.
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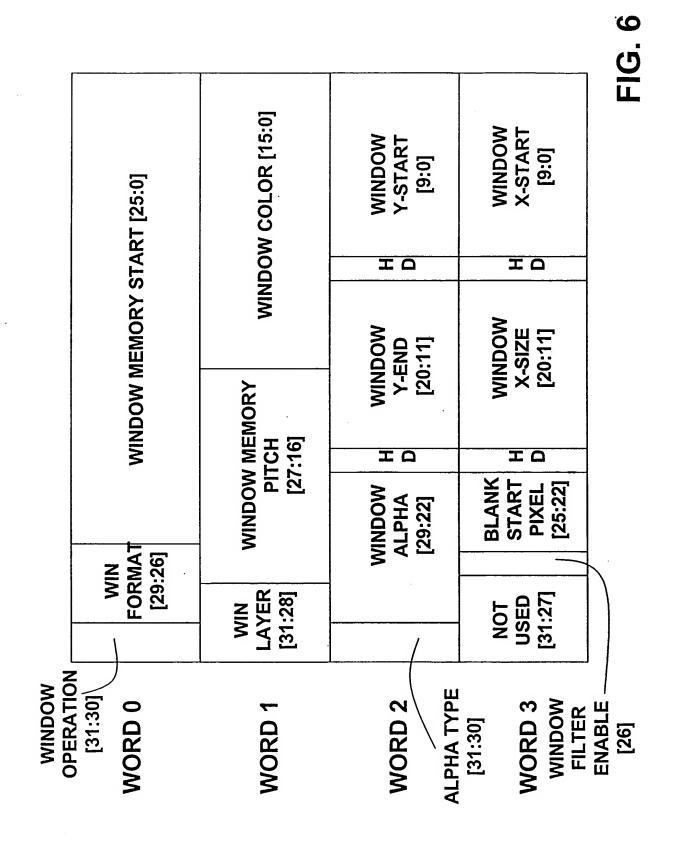
Attorney: Jun-Young E. Jeon
Docket No.: 51374/JEJ/B600
Inventor(s): Alexander G. MacInnis, et al.
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and Video Surfaces
Sheet 3 of 37 AUDIO IN AUDIO OUT VIDEO OUT FIG. 3 99 62 VEC (DENC) **AUDIO** DISPLAY ENGINE BUFFER 09 ACCELERATOR COMPOSITOR 56 54 **VIDEO** CONTROLLER MEMORY CONTROLLER MEMORY WINDOW DMA PASS THROUGH 64 **PERIPHERALS** I/O BUS BRIDGE CPU 74 50 72 TBC DECODER VIDEO SCALER 52 VIDEO MUX SRC BYPASS VIDEO IN ANALOG VIDEO IN DIGITAL VIDEO IN



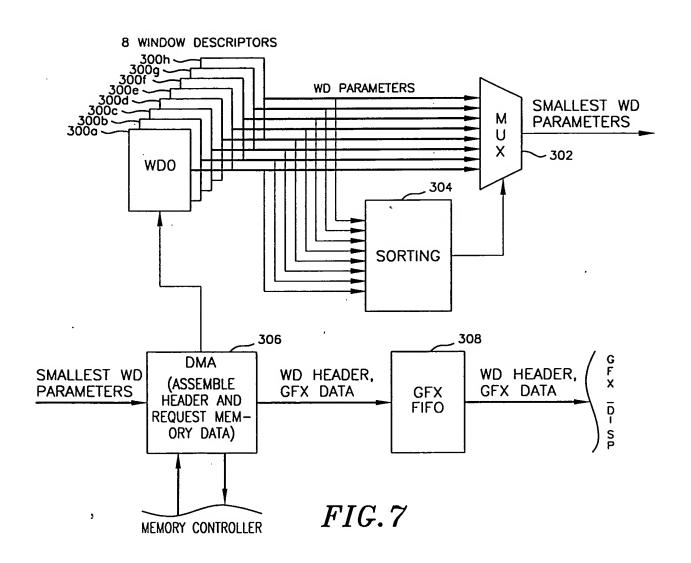
Attorney: Jun-Young E. Jeon
Docket No.: 51374/JEJ/B600
Inventor(s): Alexander G. MacInnis, et al.
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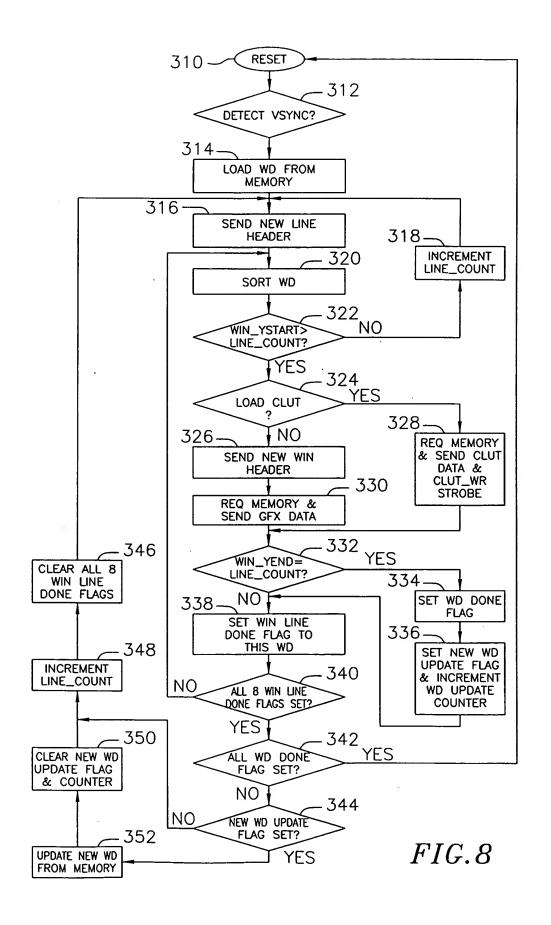
Attorney: Jun-Young E. Jeon
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Inventor(s): Alexander G. MacInnis, et al.
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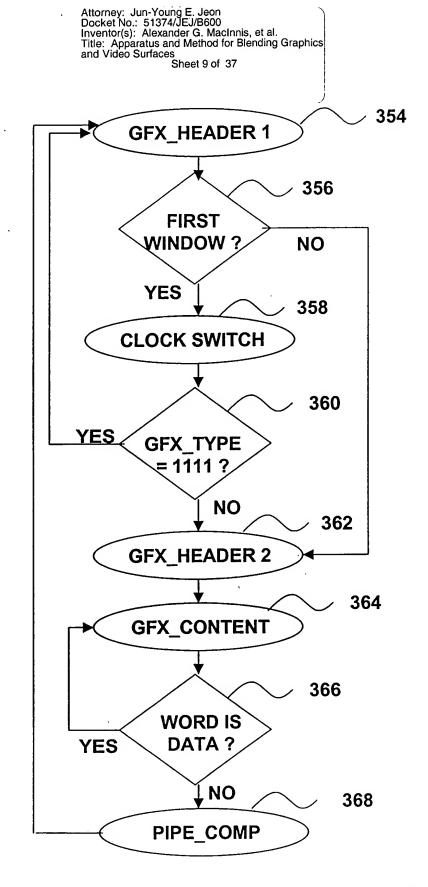
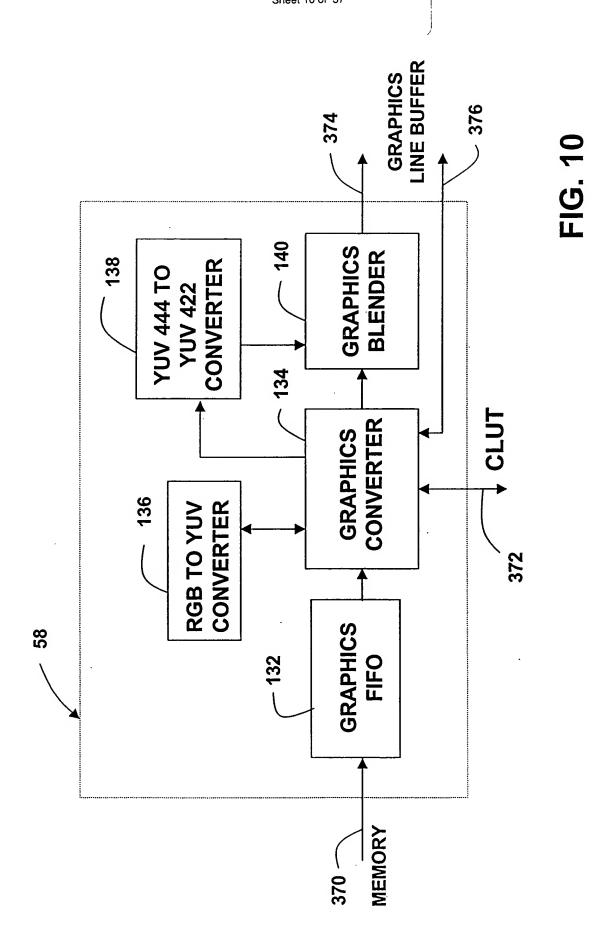


FIG. 9

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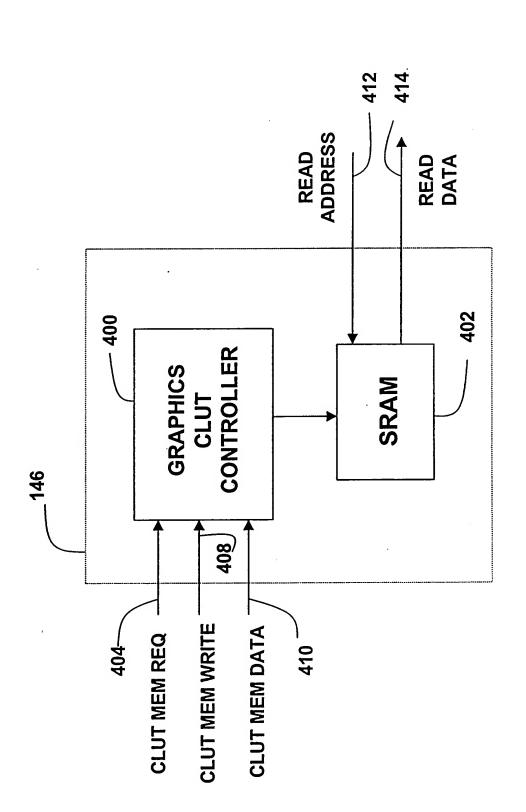


FIG. 11

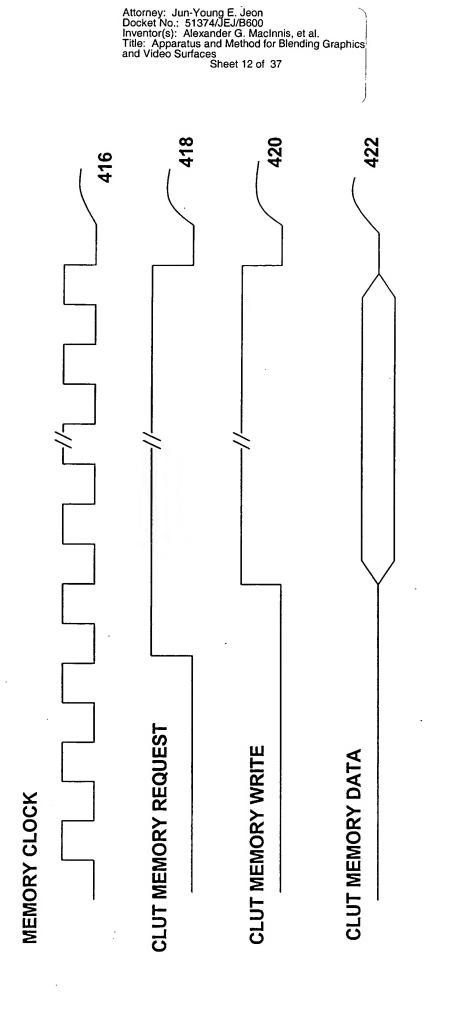
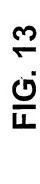
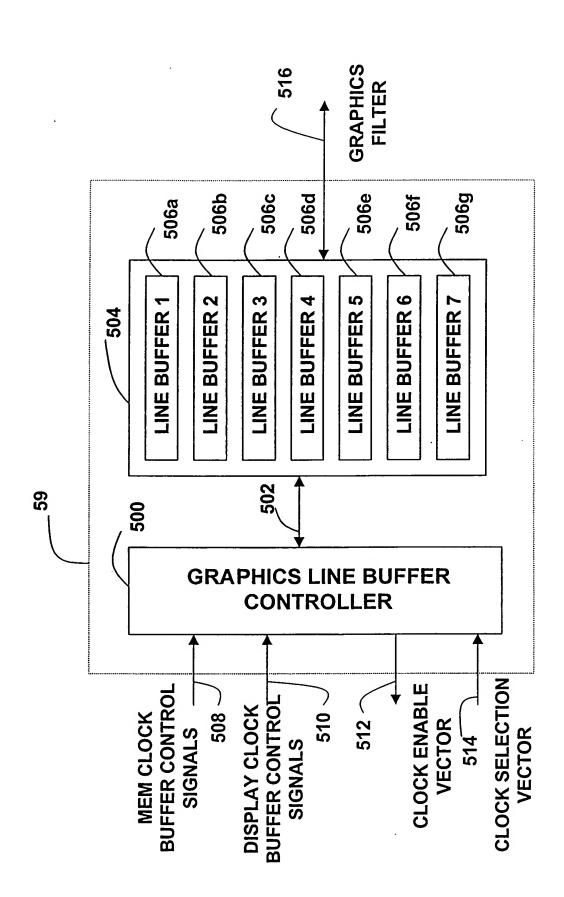
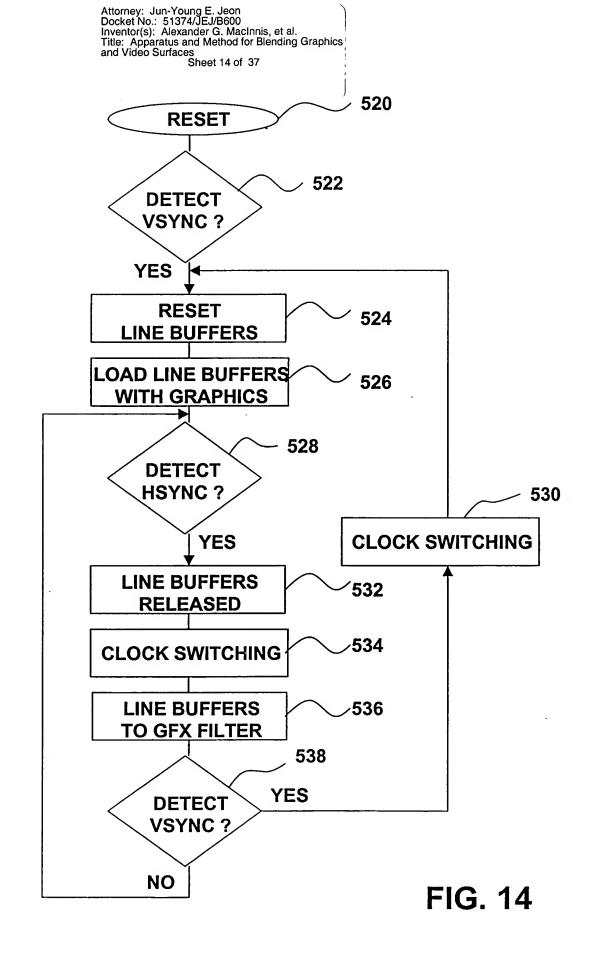


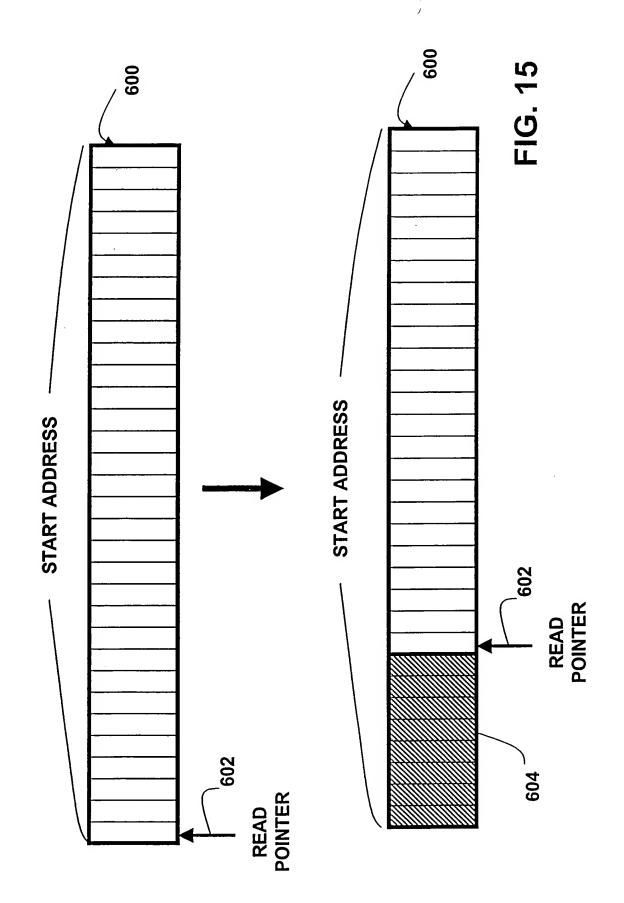
FIG 12



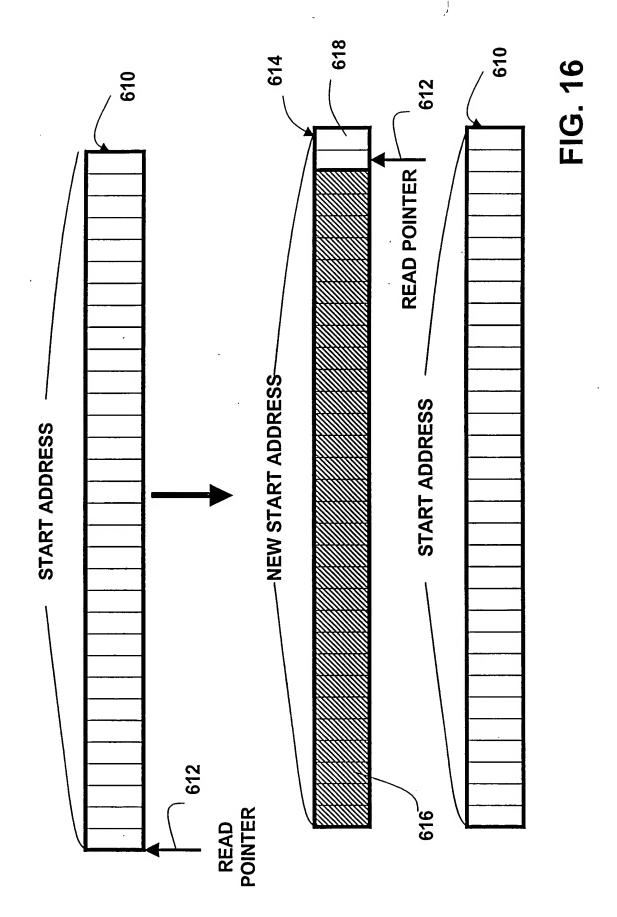




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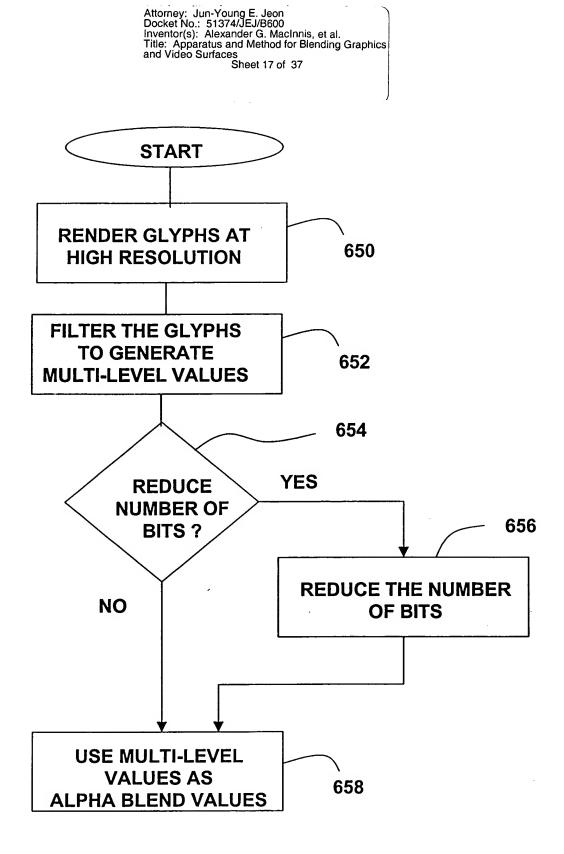


FIG. 17

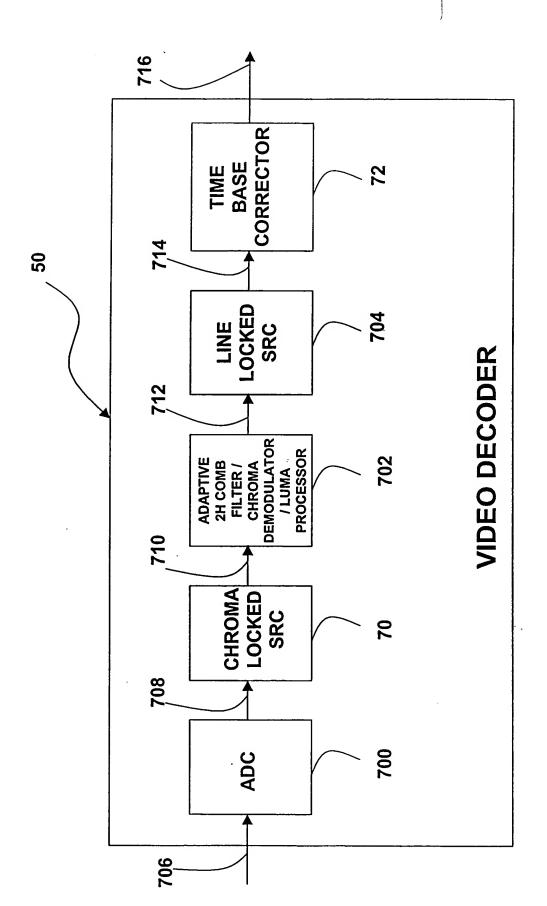


FIG. 18

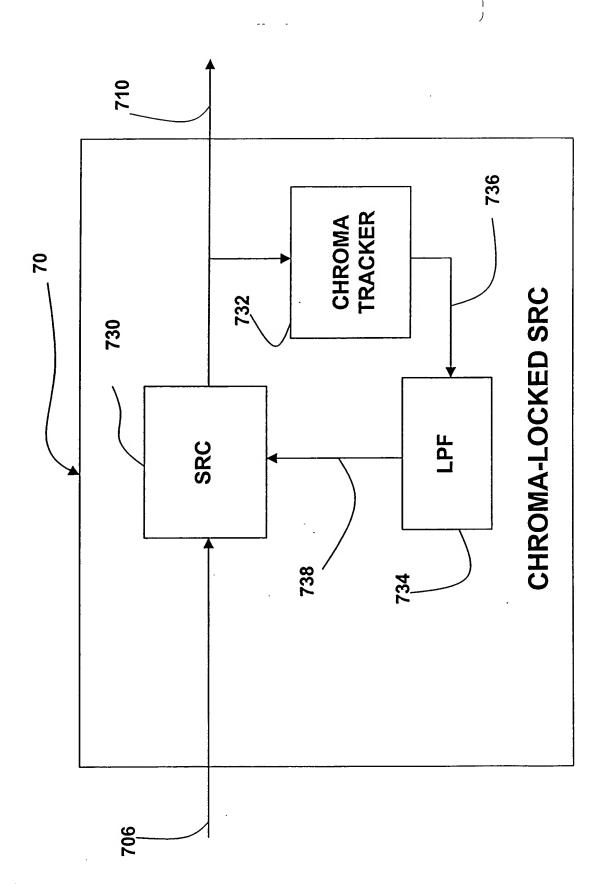


FIG. 19

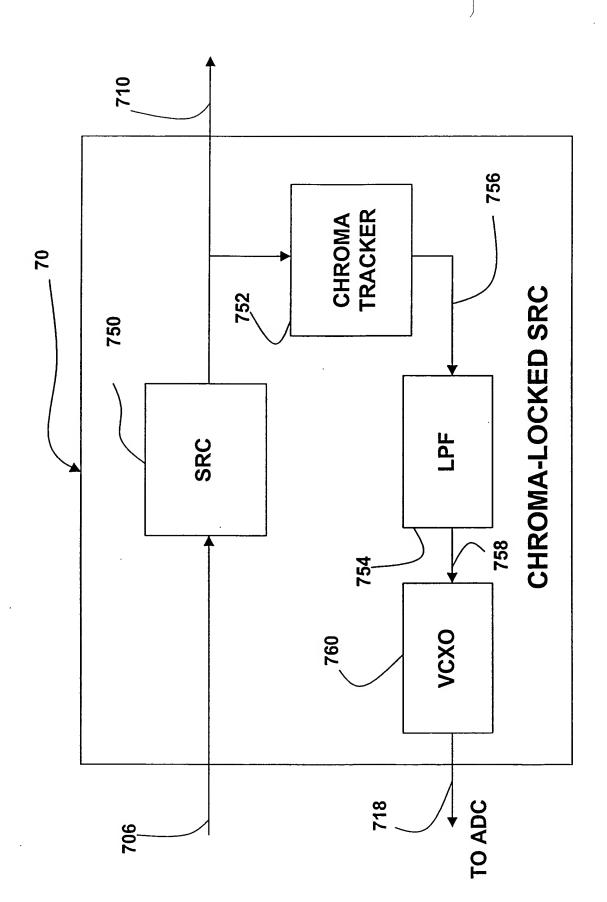


FIG. 20

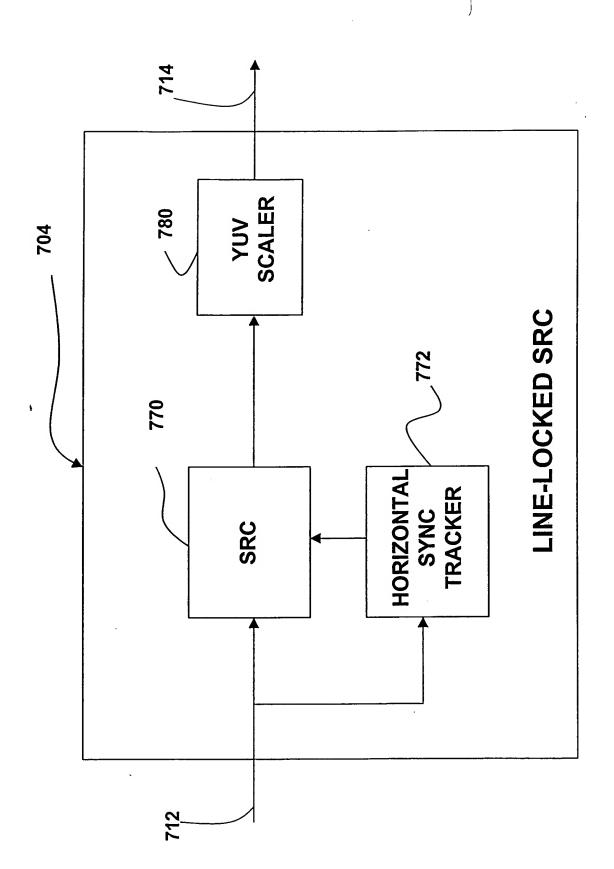
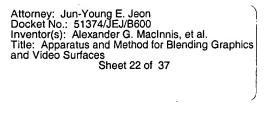
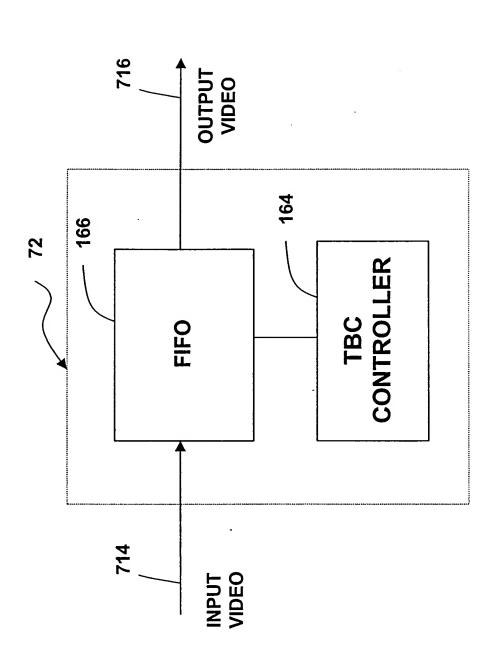


FIG. 21





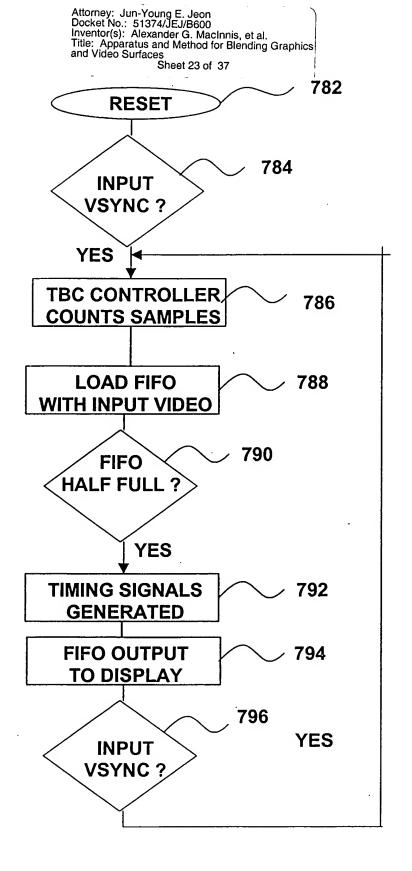


FIG. 23

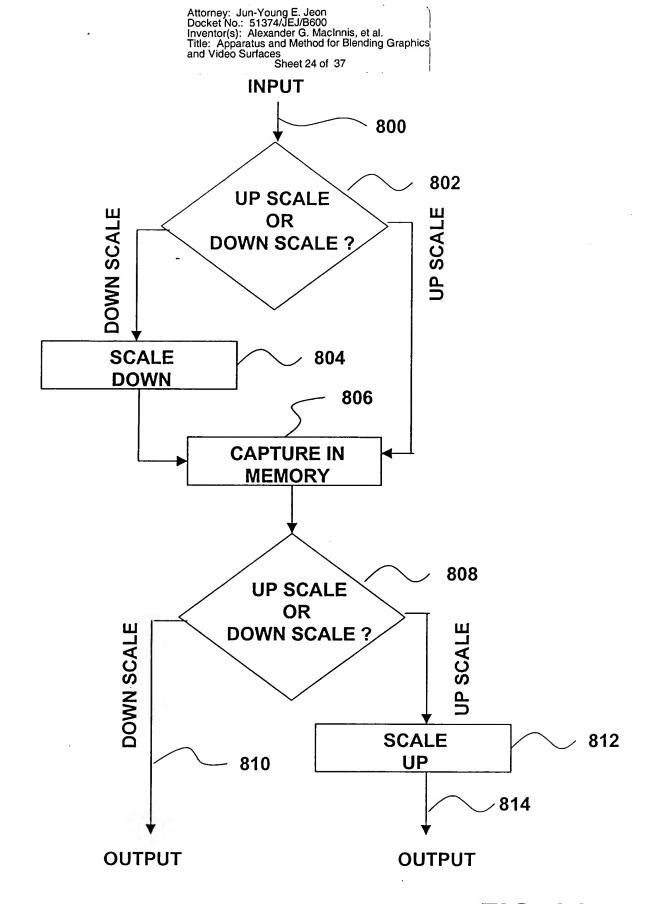
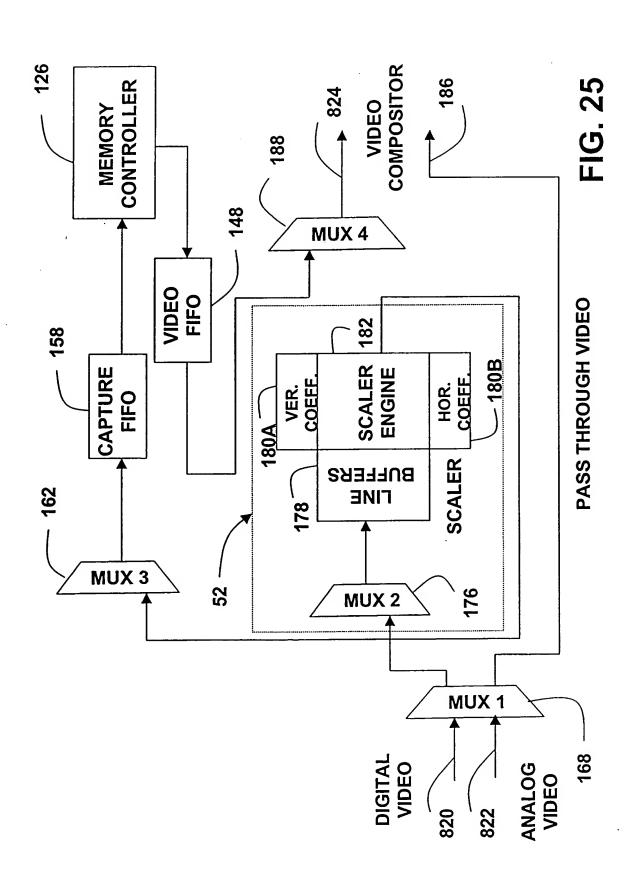
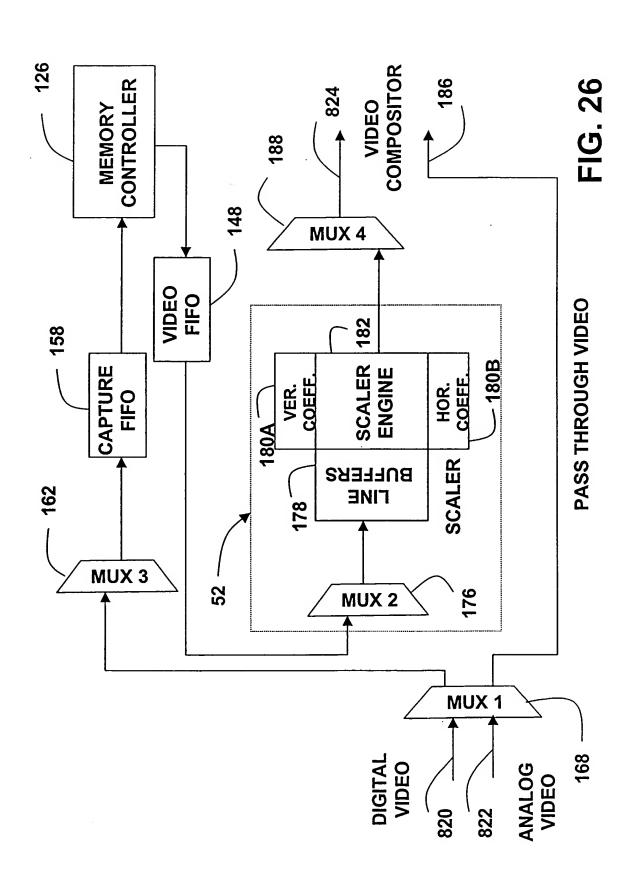


FIG. 24

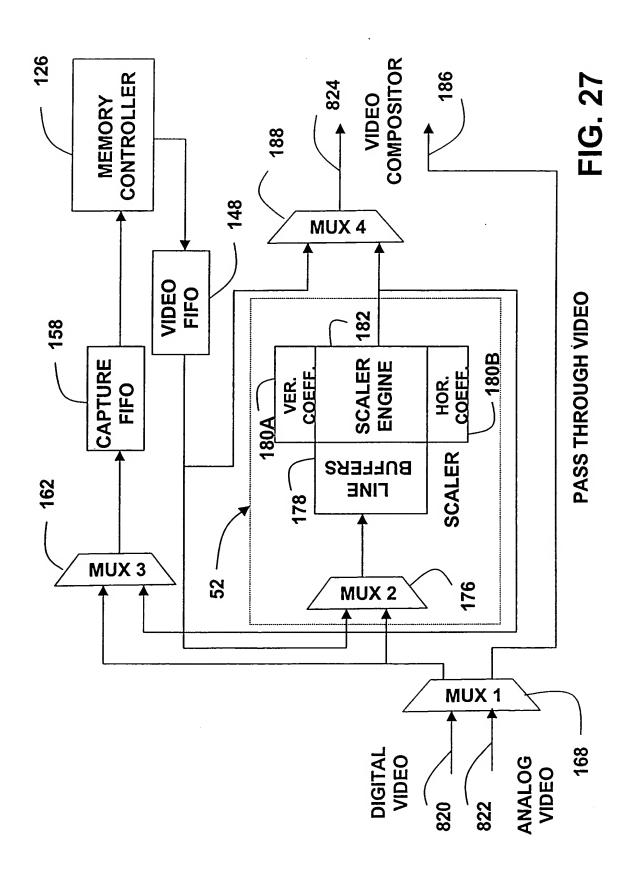
Attorney: Jun-Young E. Jeon
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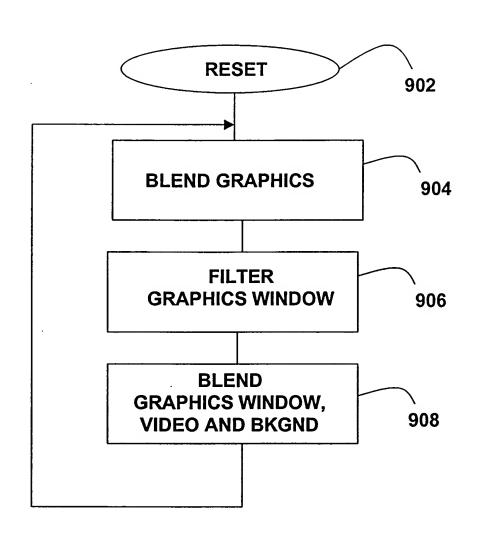
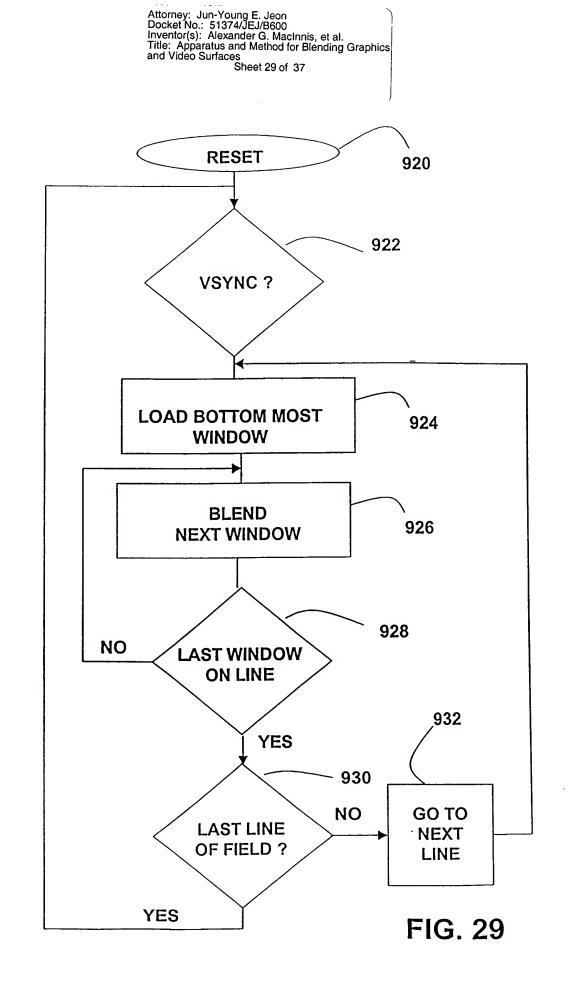
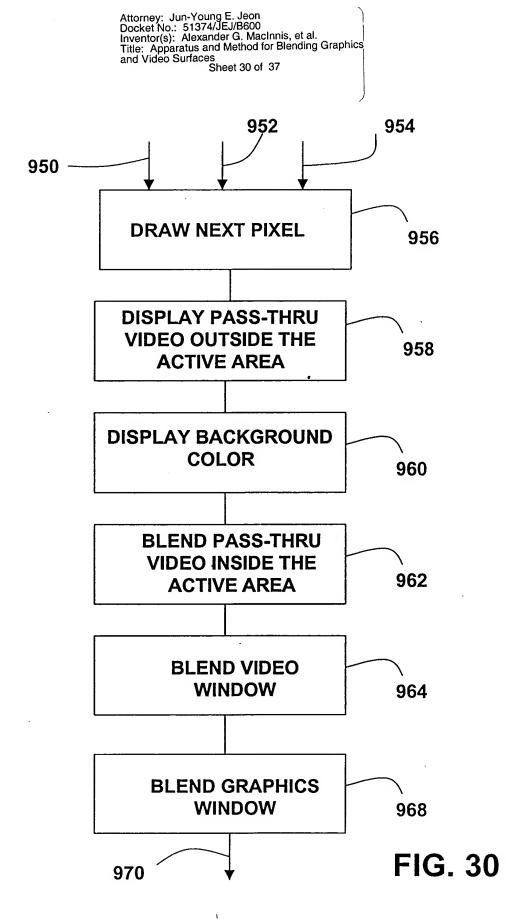
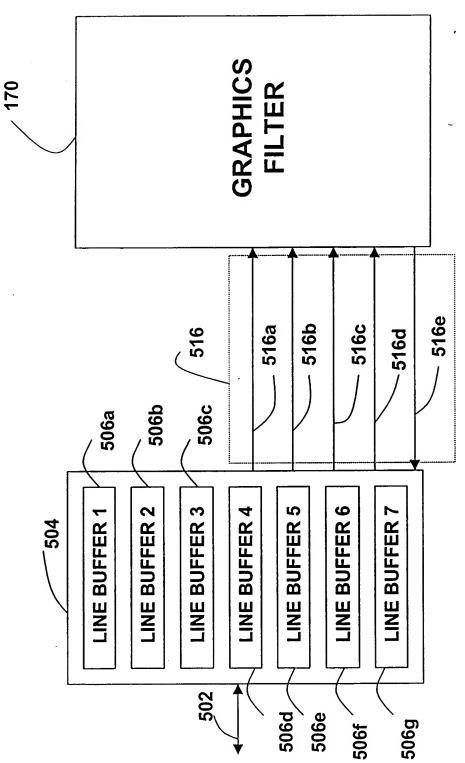


FIG. 28









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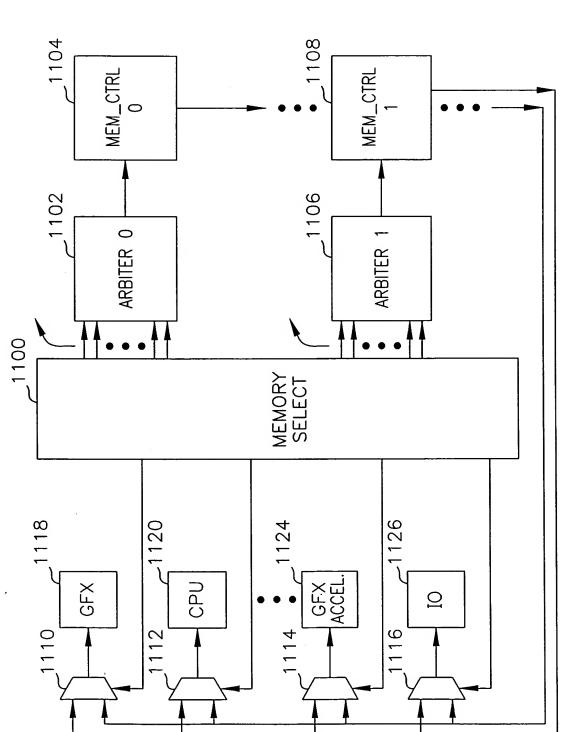
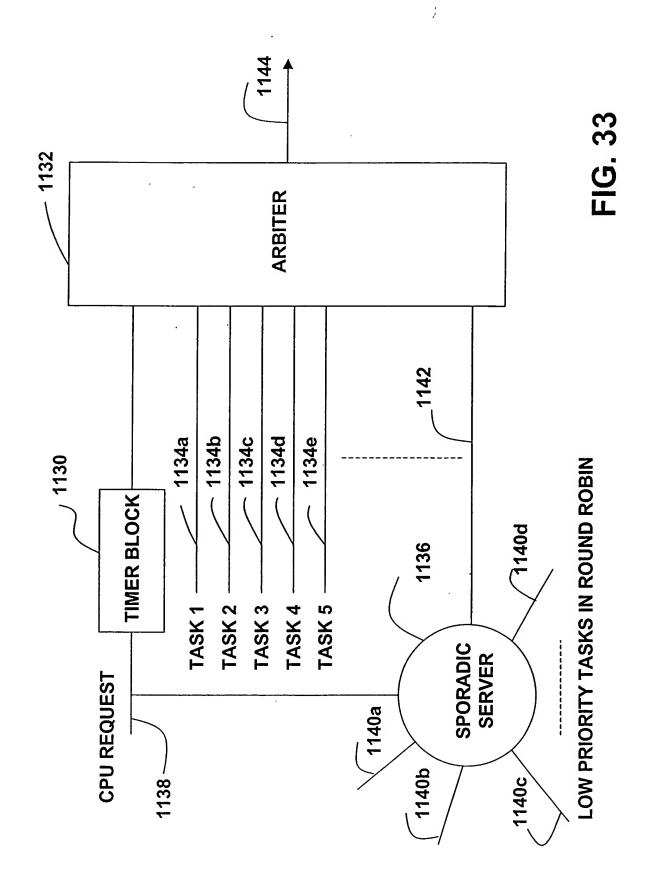
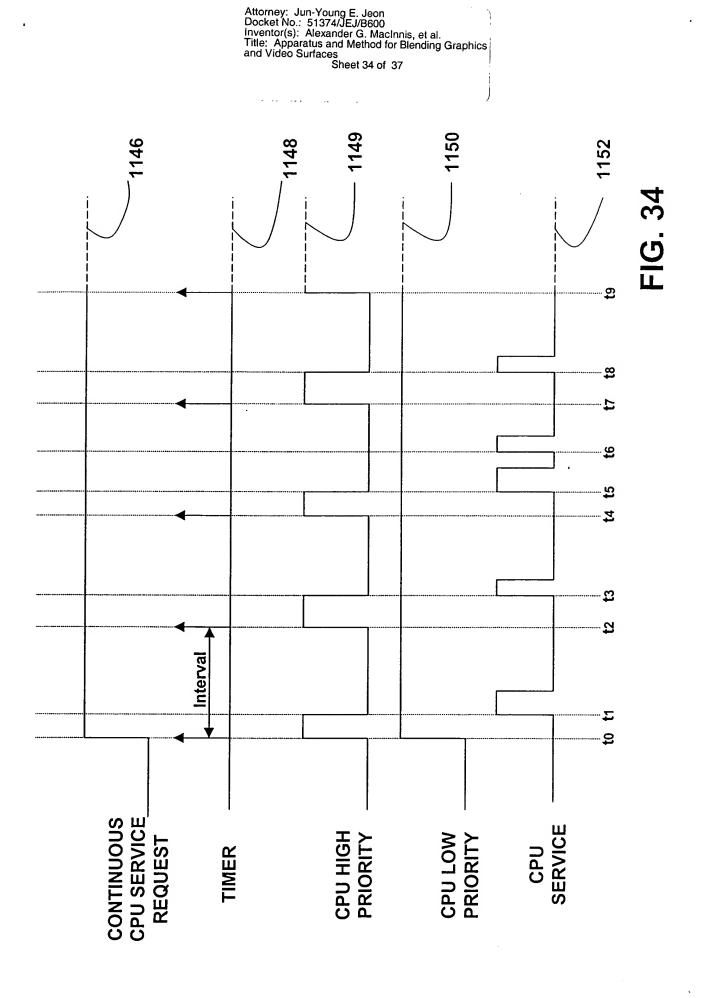
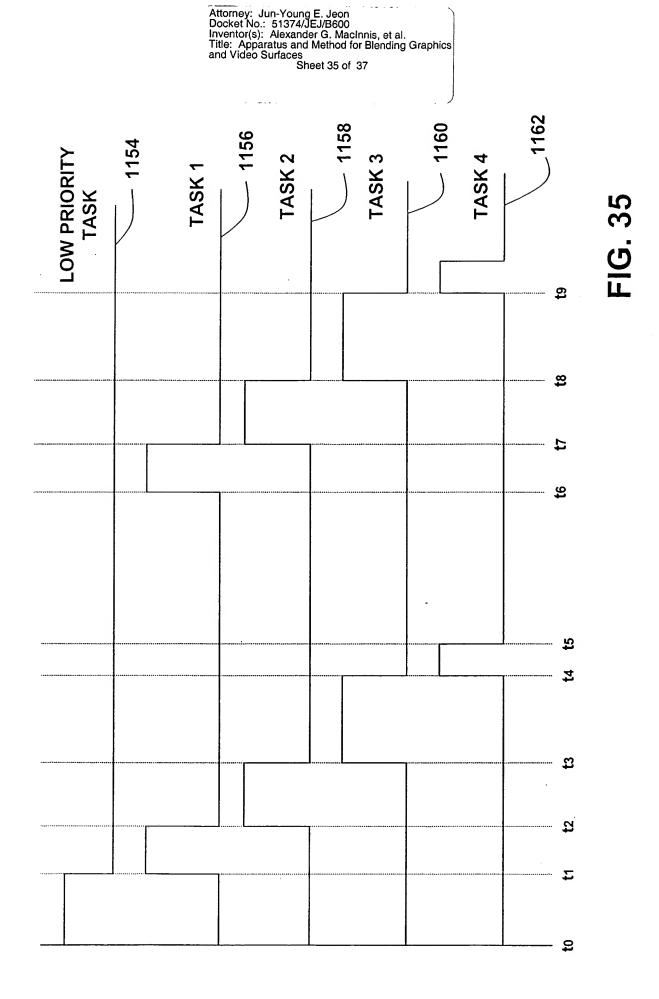


FIG.32

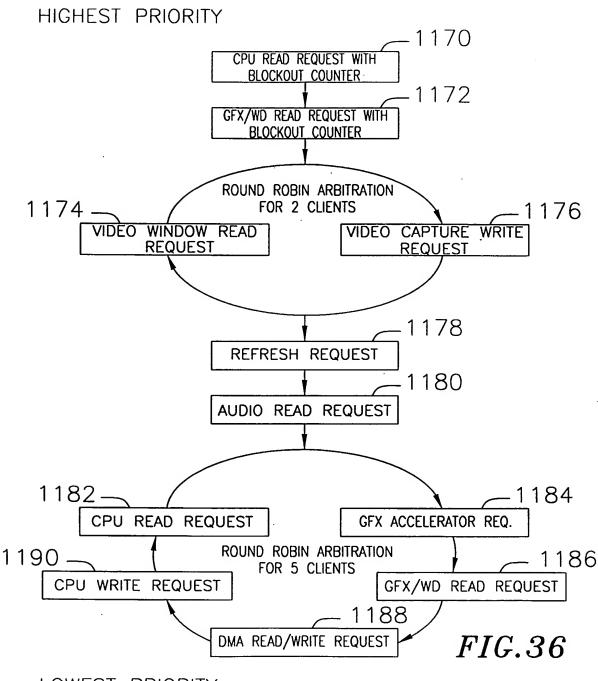
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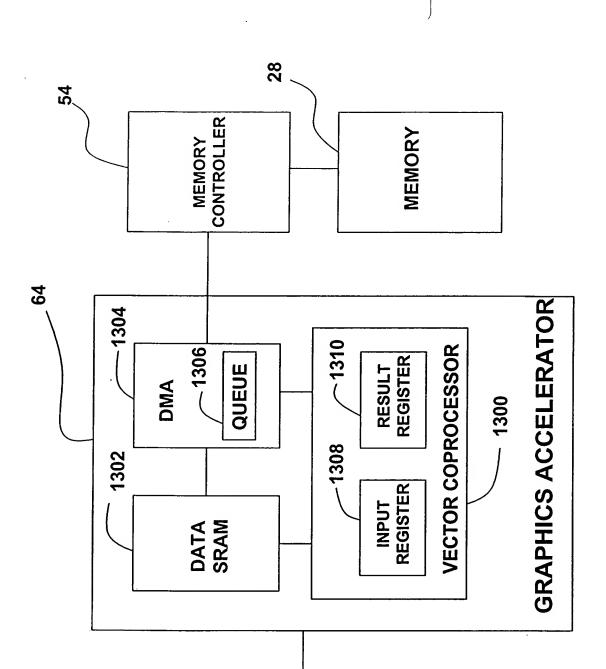




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LOWEST PRIORITY



CPU

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